Total Marks: 50

Subject Name: OS

Hi-Tech Institute of Engineering & Technology

DEPARTMENT OF MCA

(SEM- II) EVEN SEMESTER 2022-23

Subject Code: KCA-203

Time: 90 Minutes

Note: 1. Attempt all Sections. If require any missing data; then choose suitably.

SECTION-A

1. Attempt all question in brief.

Ouestion Q.No Marks CO Explain the function of Operating System. a. 2 1 What Real time OS? 2 2 b. Explain Reentrant Kernal. 2 3 c. d. What are Semaphores. 2 3 Define Multiprogramming in operating system? 2 4 e. 2 f. Define Segmentation and its types. 1 Draw the Process Transition diagram. 2 2 g. Define FTP and Telnet. 2 4 h. Define Cache Memory and Virtual memory. 2 5 i. Explain I/O devices and I/O buffering. 2 5 J.

SECTION-B

2. Attempt any FOUR of the following:

Question Marks CO Q.No Define OS with its layered structure. 10 1 a. 2 Define Concurrent process and classical problem in concurrency. 10 b. Difference between Thread and process with the help of example. 10 3 c. Define Memory management. d. 10 4 Define disk storage and disk scheduling. 10 5 e.

SECTION-C

3. Attempt any ONE part of the following:

Q.No	Question	Marks	CO
a.	What is a process and process table? What are different states of process	10	1
b.	Define process synchronization. Discuss critical section problem.	10	1

4. Attempt any ONE part of the following:

Q.No	Question	Marks	CO
a.	What is deadlock? What are the necessary conditions for deadlock?	10	2
b.	Define Sleeping Barber Problem with the help of example.	10	2

10 x 3 =30

10x1 = 10

10x1 = 10

 $2x \ 10 = 20$

Marka

5. Attempt any ONE part of the following:

Q.No	Question	Marks	CO
a.	Define CPU scheduling. Why do we need scheduling?	10	3
b.	What is deadlock detection algorithm? Explain it with example.	10	3

6. Attempt any ONE part of the following:

Q.No	Question	Marks	CO
a.	Discuss Deadlock avoidance using Banker's algorithm.	10	4
b.	Define memory management. How many type of partitions. Discuss it.	10	4

7. Attempt any ONE part of the following:

10x1 = 10

Q.No	Question	Marks	CO
a.	Consider a machine with 64 MB physical memory and a 32-bit virtual address space. If the page size is 4KB, what is the approximate size of the page table?	10	5
b.	Define Access matrix. How it can be implemented	10	5

10x1 = 10

10x1 = 10